

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) An electronic circuit comprising:

- a first power source line; and

- a plurality of unit circuits,

- each of the plurality of unit circuits including:

- a first transistor that is coupled to an electronic element and that is coupled to the first power source line;

- a second transistor that controls an electrical connection between a drain of the first transistor and a gate of the first transistor; and

- a third transistor that controls an electrical connection between the first transistor and a current source that outputs a data current that sets a conduction state of the first transistor,

- the first power source line being electrically disconnected from a driving potential during at least a part of a first period in which the third transistor is in an on-state,

- a driving current flowing through the electronic element during at least a part of a second period in which the third transistor is in an off-state,

- a source or a drain of the first transistor being electrically connected to the driving potential during at least a part of the second period, and

- the driving current having a level corresponding to the conduction state of the first transistor set by the data current.

2. (Previously Presented) An electronic circuit comprising:

- a first power source line; and

- a plurality of unit circuits,

each of the plurality of unit circuits including:

a first transistor that is coupled to an electronic element and that is coupled to the first power source line;

a second transistor that controls an electrical connection between a drain of the first transistor and a gate of the first transistor; and

a third transistor that controls an electrical connection between the first transistor and a current source that outputs a data current that sets a conduction state of the first transistor,

the data current flowing through the first transistor during at least a part of a first period in which the third transistor is in an on-state,

a potential of the first power source line being set to a first voltage during at least a part of the first period,

a driving current flowing through the first transistor during at least a part of a second period in which the third transistor is in an off-state,

the driving current having a level corresponding to the conduction state of the first transistor set by the data current,

the potential of the first power source line being set to a second voltage that is different from the first voltage during at least a part of the second period, and

a source or a drain of the first transistor being electrically connected to the first power source line during at least a part of the second period.

3. (Previously Presented) An electronic circuit comprising:

a plurality of unit circuits; and

a first power source line,

each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal and a fourth terminal; and

a third transistor having a fifth terminal and a sixth terminal, the fifth terminal being coupled to the first terminal,

a conduction state between the first terminal and the second terminal being set according to a voltage of the first control terminal,

the first terminal being coupled to the first power source line, and

a potential of the first power source line being set to a plurality of potential levels or an electrical connection between the first power source line and a driving voltage being controlled.

4. (Previously Presented) An electronic circuit comprising:

a first power source line;

a control circuit that sets the potential of the first power source line to a plurality of potential levels or controls an electrical connection between a driving voltage and the first power source line; and

a plurality of unit circuits,

each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal, the second transistor controlling an electrical connection between the second terminal and the first control terminal;

a third transistor having a fifth terminal and a sixth terminal, the fifth terminal being coupled to the first terminal; and

a capacitive element having a seventh terminal and an eighth terminal,  
the seventh terminal being coupled to the first control terminal and the third terminal,

a conduction state between the first terminal and the second terminal being set  
according to a voltage of the first control terminal, and

the first terminal being connected to the first power source line.

5. (Currently Amended) An electronic circuit comprising:

a first power source line;

a second power source line that is held at a predetermined potential;

a control circuit that sets the potential of the first power source line to a  
plurality of potential levels or controls an electrical connection between a driving voltage and  
the first power source line; and

a plurality of unit circuits,

each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first  
control terminal;

a second transistor having a third terminal and a fourth terminal, the  
third terminal being coupled to the first control terminal, the second transistor controlling an  
electrical connection between the second terminal and the first control terminal;

a third transistor having a fifth terminal and a sixth terminal, the fifth  
terminal being coupled to the first terminal; and

a capacitive element having a seventh terminal and an eighth terminal,  
the seventh terminal being coupled to the first control terminal and the third terminal,

a conduction state between the first terminal and the second terminal being set  
according to a voltage of the first control terminal,

the first terminal being coupled to ~~a~~the first power source line together with the first terminals of other unit circuits of the plurality of unit circuits, and

the eighth terminal being coupled to the second power source line.

6. (Previously Presented) The electronic circuit according to Claim 1, transistors included in each of the unit circuits including only the first transistor, the second transistor, and the third transistor.

7. (Previously Presented) The electronic circuit according to Claim 3, an electronic element being coupled to the second terminal.

8. (Previously Presented) The electronic circuit according to Claim 1, the electronic element being a current-driven element.

9. (Previously Presented) The electronic circuit according to Claim 4, each of the control circuits being a fourth transistor having a ninth terminal and a tenth terminal,

the ninth terminal being coupled to the driving voltage, and

the tenth terminal being coupled to the first power source line.

10. (Currently Amended) A method of driving an electronic circuit that has a plurality of ~~unit circuits and a plurality of~~ first power source lines and a plurality of unit circuits each of which includes:

a first transistor that is coupled to one first power source line of the plurality of first power source lines;

a second transistor that controls an electrical connection between a drain of the first transistor and a gate of the first transistor; and

a third transistor that controls an electrical connection between the first transistor and a current source that outputs a data current that sets a conduction state of the first transistor,

the method comprising:

supplying the data current to the first transistor through the third transistor, and  
supplying a driving current whose level corresponds to the conduction state of  
the first transistor to an electronic element, the driving current flowing through the first  
transistor,

the one first power source line being electrically disconnected from a driving  
voltage during at least a part of a first period in which ~~in~~ the supplying of the data current to  
the first transistor is performed, and

the driving voltage being applied to a drain of the first transistor or a source of  
the first transistor through the first power source line during at least a part of a second period  
in which the supplying of the driving current to the electronic element is performed.

11. (Previously Presented) A method of driving an electronic circuit that has a  
plurality of first power source lines and a plurality of unit circuits, each of which includes:

a first transistor that has a first terminal, a second terminal, and a first control  
terminal, the first terminal being coupled to one first power source line of the plurality of the  
first power source lines;

a second transistor that has a third terminal and a fourth terminal, the third  
terminal being coupled to the first control terminal, and the fourth terminal being coupled to  
the second terminal;

a third transistor that has a fifth terminal and a sixth terminal, the fifth terminal  
being coupled to the first terminal; and

a capacitive element that has a seventh terminal and an eighth terminal, the  
seventh terminal being coupled to the first control terminal and the third terminal,

the method comprising:

supplying an electric charge to the capacitive element, a quantity of the electric charge corresponding to a data current flowing through the third transistor; and

supplying a driving current to an electronic element, the driving current flowing between the one first power source line and the electronic element through the first transistor, and the driving current having a level corresponding to the quantity of the electric charge,

the one first power source line being electrically disconnected from a driving voltage during at least a part of a first period in which the supplying of the electric charge to the capacitive element is performed, and

the driving voltage being applied to the first terminal of the first transistor through the one first power source line during at least a part of a second period in which the supplying of the driving current to the electronic element is performed.

12. (Previously Presented) A method of driving an electronic circuit that has a plurality of first power source lines, a plurality of second power source lines and a plurality of unit circuits each of which includes:

a first transistor that has a first terminal, a second terminal, and a first control terminal, the first terminal being coupled to one first power source line of the plurality of first power source lines;

a second transistor that has a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal, the fourth terminal being coupled to the second terminal;

a third transistor that has a fifth terminal and a sixth terminal, the fifth terminal being coupled to the first terminal; and

a capacitive element that has a seventh terminal and an eighth terminal, the seventh terminal being coupled to the first control terminal and the third terminal and the

eighth terminal being coupled to one second power source line of the plurality of second power source lines,

the method comprising:

supplying an electric charge to the capacitive element, a quantity of the electric charge corresponding to a data current flowing through the third transistor, and

supplying a driving current to an electronic element, the driving current flowing between the one first power source line and the electronic element through the first transistor, and the driving current having a level corresponding to the quantity of the electric charge,

the one first power source line being electrically disconnected from a driving voltage during at least a part of a first period in which the supplying of the electric charge to the capacitive element is performed,

the driving voltage being applied to the first terminal of the first transistor through the one first power source line during at least a part of a second period in which the supplying of the driving current to the electronic element is performed, and

the one second power source line being held at a predetermined voltage during the first period and the second period.

13-21. (Cancelled)

22. (Original) An electronic apparatus equipped with the electronic circuit according to Claim 1.

23. (Cancelled)

24. (Previously Presented) The method according to claim 11,  
the data current flowing through the first transistor.

25. (Previously Presented) A method of driving an electronic circuit that has a plurality of unit circuits and a plurality of first power source lines each of which includes:



a first transistor that has a first terminal, a second terminal, and a first control terminal, the first terminal being coupled to one first power source line of the plurality of first power source lines;

a second transistor that has a third terminal and a fourth terminal, the third terminal being coupled to the first control terminal, and the fourth terminal being coupled to the second terminal;

a third transistor that has a fifth terminal and a sixth terminal, the fifth terminal being coupled to the first terminal; and

a capacitive element that has a seventh terminal and an eighth terminal, the seventh terminal being coupled to the first control terminal and the third terminal,

the method comprising:

supplying an electric charge to the capacitive element, a quantity of the electric charge corresponding to a data signal supplied through the third transistor, and

supplying a driving current to an electronic element, the driving current flowing between the one first power source line and the electronic element through the first transistor, and the driving current having a level corresponding to the quantity of the electric charge,

the one first power source line being electrically disconnected from a driving voltage during at least a part of a first period in which the supplying of the electric charge to the capacitive element is performed, and

the driving voltage being applied to the first terminal of the first transistor through the one first power source line during at least a part of a second period in which the supplying of the driving current to the electronic element is performed.